

Filter

Kerut (*ripple*).

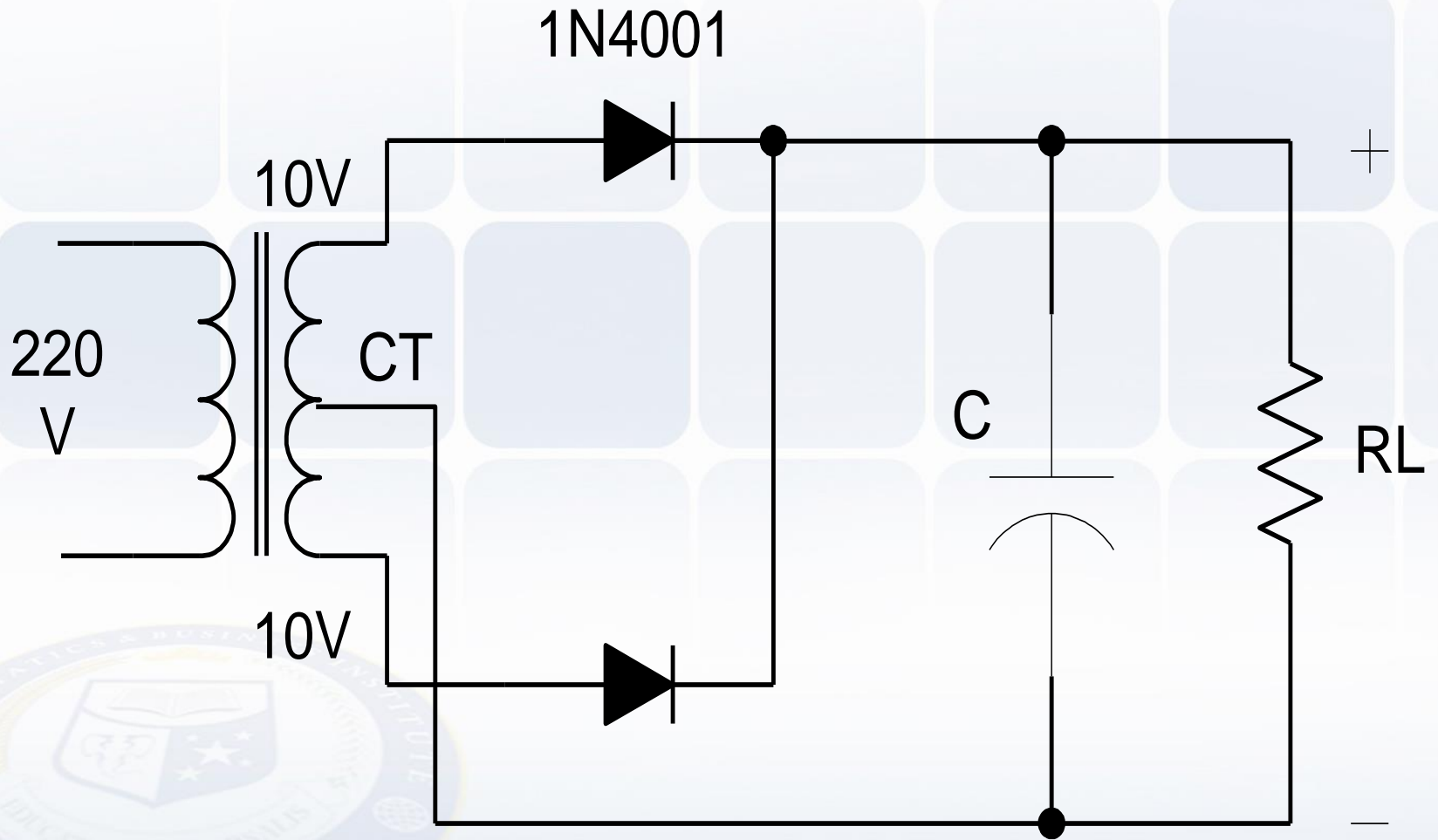
Filter *choke* dan kapasitor

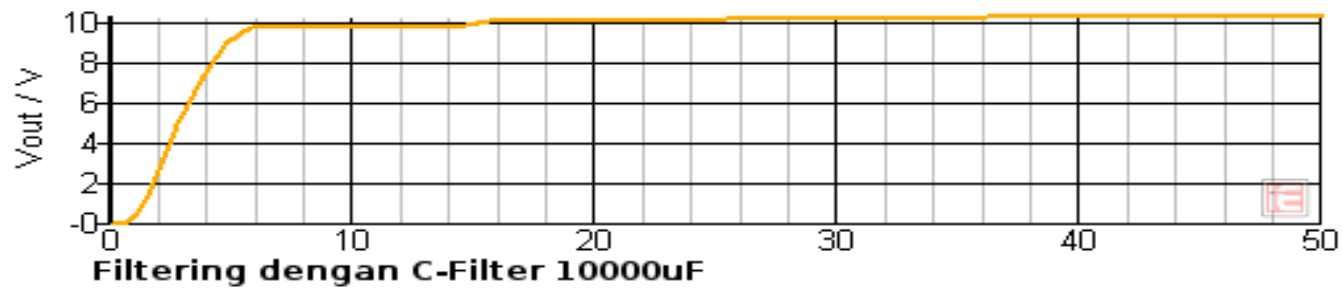
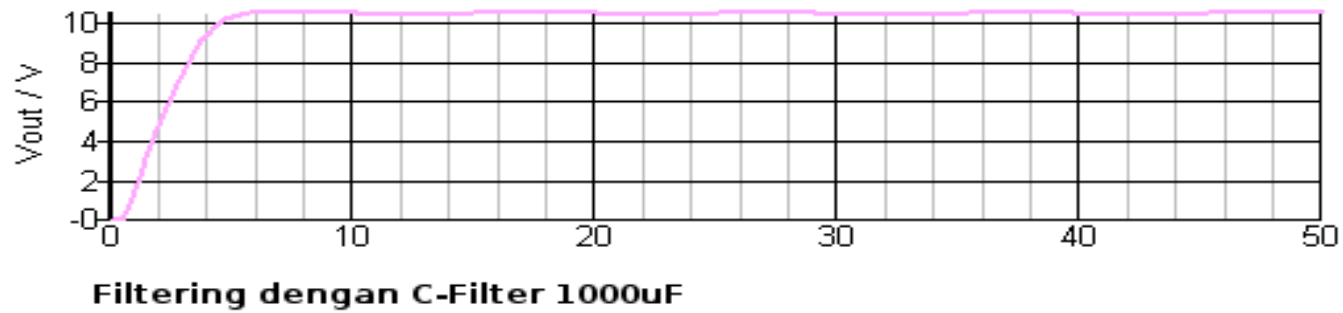
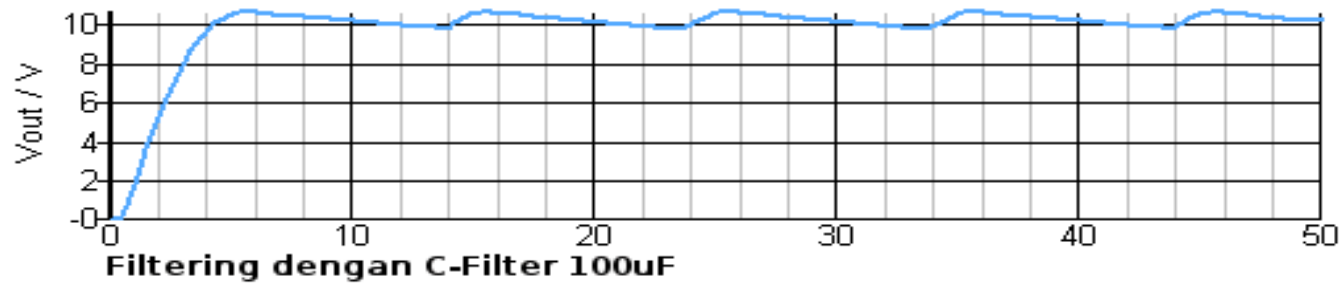
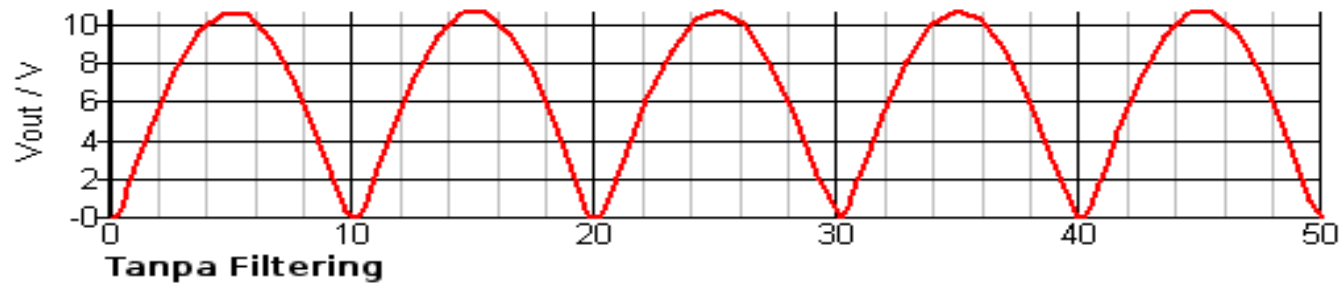
$$X_L = 2 \pi f L$$

**Sulit dilalui arus bolak-balik,
reaktansi besar.**

$$X_C = 1 / 2 \pi f C$$

**Mudah dilalui arus bolak-balik,
reaktansi kecil**





Kondensator elektrolit tersedia kapasitas. Polaritas Kapasitor

***ripple* puncak-ke puncak :**

$$V_R = I_L / fC$$

Di mana : V_R = tegangan *ripple*

I_L = Arus beban DC

f = frekuensi *ripple*

C = Kapasitas kapasitor

Contoh

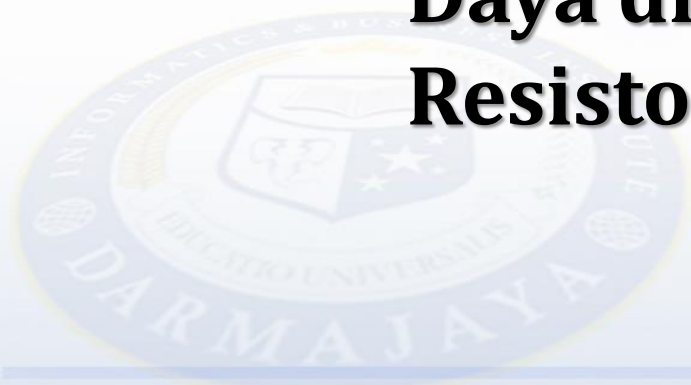
$$\begin{aligned} V_L = V_P &= V_{\text{rms}} / 0,707 \\ &= 12 / 0,707 = 17 \text{ V} \end{aligned}$$

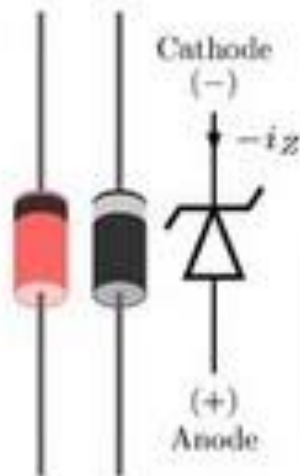
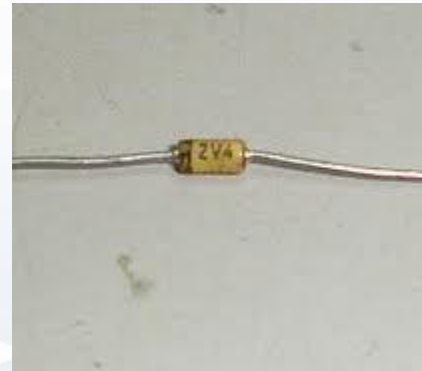
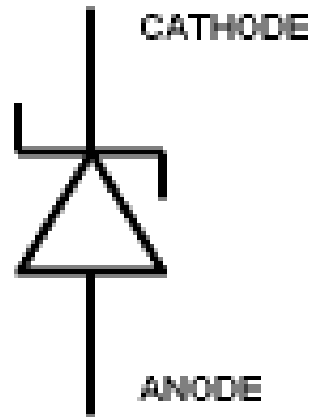
$$\begin{aligned} I_L &= V_L / R_L \\ &= 17 / 5 \cdot 10^3 = 0,0034 \text{ A} = 3,4 \text{ mA} \end{aligned}$$

$$\begin{aligned} V_R &= I_L / fC \\ &= 0,0034 / 100 \cdot 100 \cdot 10^{-6} = 0,34 \text{ V}_{\text{PP}} \end{aligned}$$

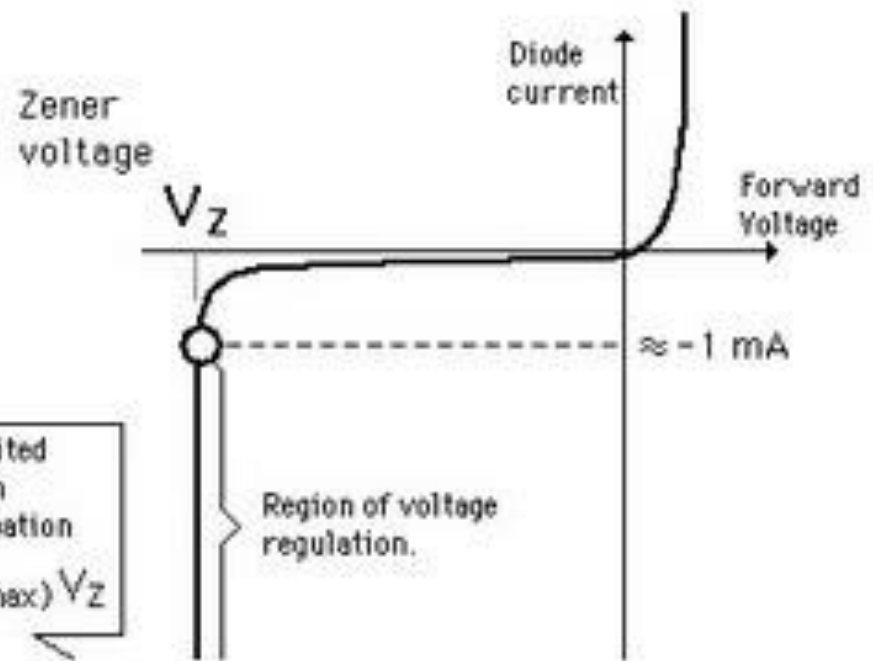
V_P dikurangi *potenitial barrier* dioda ; 0,7 sistem CT dan 1,4 untuk system jembatan.

Fungsi regulasi tegangan.
Regulasi dengan dioda zener.
Tegangan input.
Tegangan output.
Arus beban.
Daya dioda zener.
Resistor seri.

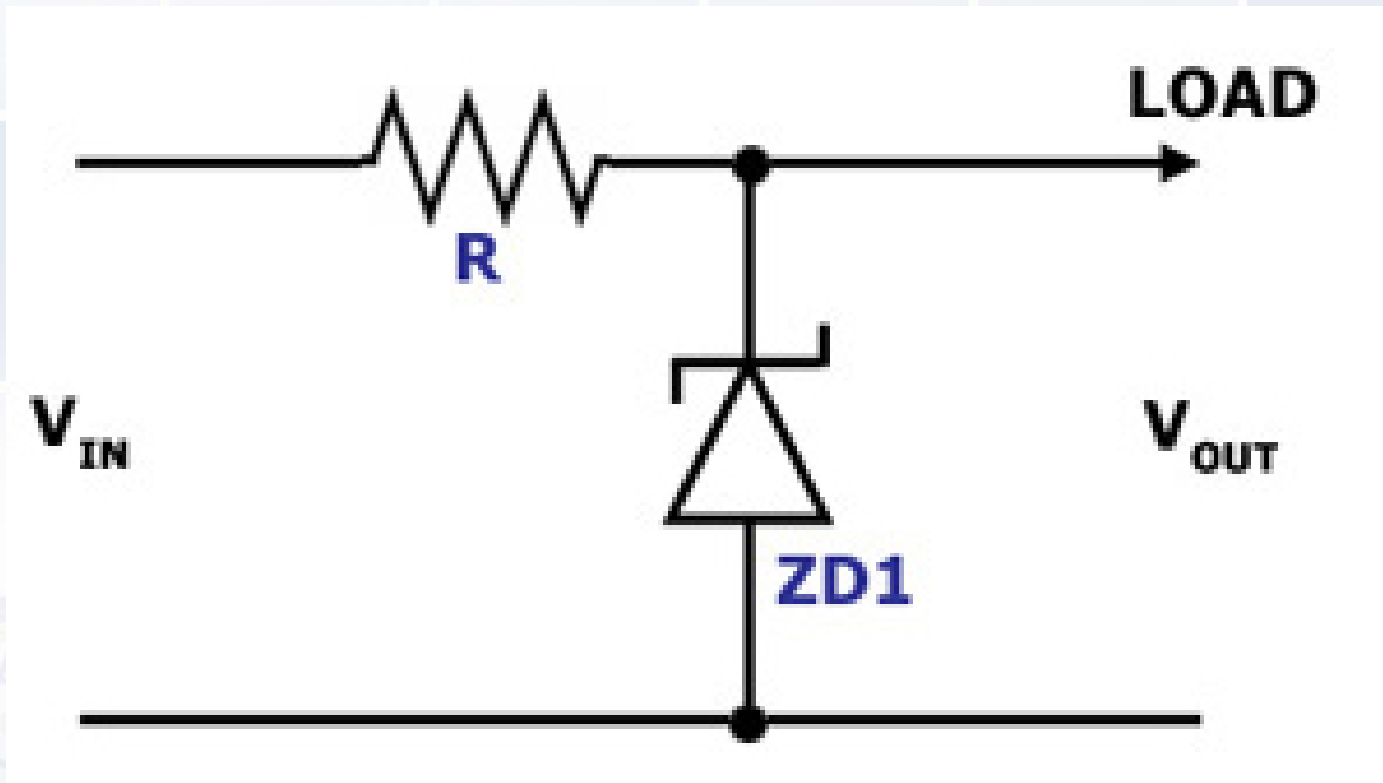




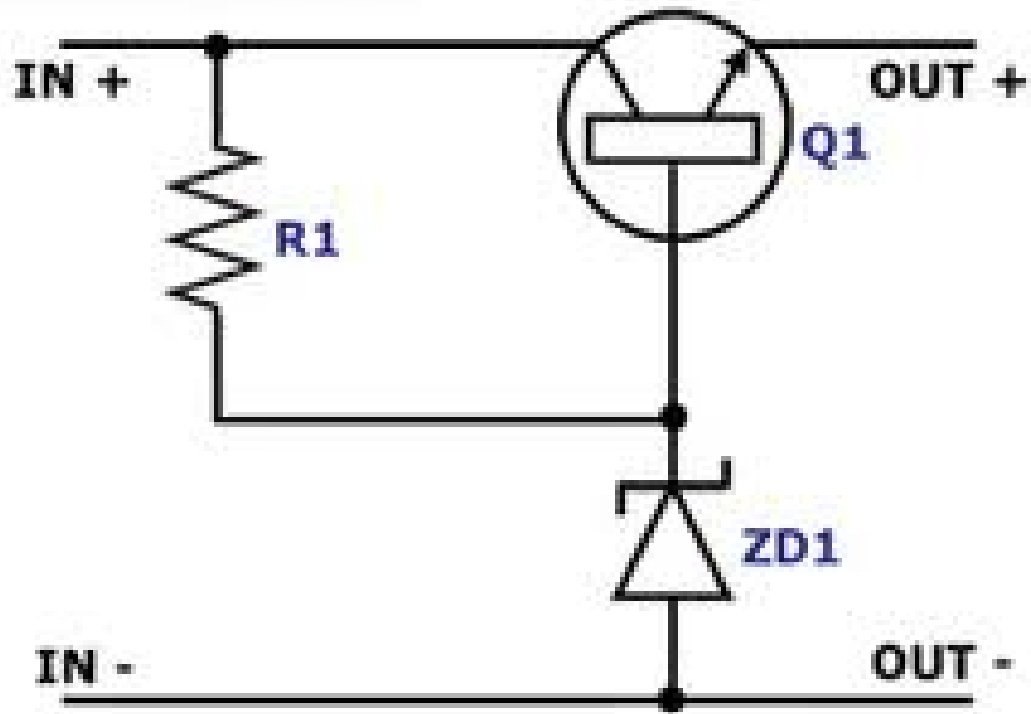
Current limited
by maximum
power dissipation
 $P_{max} = I_{z(max)} V_Z$



Regulator zener untuk arus kecil



Regulator zener & transistor



end

